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APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\* SDF  
 This application is a DIV of 09/016,999 02/02/1998 PAT 6,596,572  
 which is a DIV of 08/623,506 03/28/1996 PAT 5,767,529

\*\* FOREIGN APPLICATIONS \*\*\*\*\* SDF  
 JAPAN 7-96266 03/28/1995

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 8	TOTAL CLAIMS 30	INDEPENDENT CLAIMS 4
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after allowance	EXAMINER'S SIGNATURE <i>Monetta Lane SDF</i>			
Verified and Acknowledged	Initials			

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TITLE  
 Method of manufacturing a semiconductor method of manufacturing a thin-film transistor and thin-film transistor

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